



# Arm<sup>®</sup> Debugger

Version 6.8.1

## Release Note

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## Arm® Debugger Release Note

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This document (109667\_6.8.1\_00\_en) was issued on 2026-02-27. There might be a later issue at <https://developer.arm.com/documentation/109667>

The product version is 6.8.1.

See also: [Proprietary notice](#) | [Product and document information](#) | [Useful resources](#)

### Start reading

If you prefer, you can skip to [the start of the content](#).

### Intended audience

This document is for users of Arm Debugger that want to see details of the latest changes.

### Inclusive language commitment

Arm values inclusive communities. Arm recognizes that we and our industry have used language that can be offensive. Arm strives to lead the industry and create change.

We believe that this document contains no offensive language. To report offensive language in this document, email [terms@arm.com](mailto:terms@arm.com).

### Feedback

Arm welcomes feedback on this product and its documentation. To provide feedback on the product, create a ticket on <https://support.developer.arm.com>.

To provide feedback on the document, fill the following survey: <https://developer.arm.com/documentation-feedback-survey>.

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# 1. Release overview

The following sections describe the product and its quality status at the time of release.

## 1.1 Product description

Arm® Debugger is a tool that supports software development on targets based on Arm® processors and Fast Models such as Fixed Virtual Platform (FVP) models.

Arm Debugger includes support for System on Chip (SoC) bring-up with platform configuration utilities using the Arm® ULINK™ and Arm® DSTREAM debug probe families. You can also use third-party physical and virtual debug probes, which support a wide range of hardware and software development.

Arm Debugger integrates with Arm® Development Studio. It is also available as a Visual Studio Code extension compatible with the Keil® Studio Pack for Arm® Keil MDK v6.

For a complete list of supported processors, see [Supported Processor Cores](#).

## 1.2 Product quality

These deliverables are being released under the terms of the agreement between Arm and each licensee (the “Agreement”). All planned verification and validation is complete. The release is suitable for volume production under the terms of the Agreement.

## 2. Arm Debugger 6.8.1 requirements

Arm® Debugger 6.8.1 is compatible with the following Arm Integrated Development Environments (IDEs):

- Arm® Development Studio 2025.1-1
- Arm® Keil® Studio for Visual Studio Code, part of Arm Keil MDK v6

Arm Debugger 6.8.1 supports the following host platforms:

- Windows 10 (x86\_64)



Support for Windows 10 is deprecated and will be removed from a future release.

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- Windows 11 (x86\_64)
- Red Hat Enterprise Linux 8 Workstation (x86\_64)
- Red Hat Enterprise Linux 9 Workstation (x86\_64)
- Ubuntu Desktop Edition 22.04 LTS (x86\_64 and AArch64)
- Ubuntu Desktop Edition 24.04 LTS (x86\_64 and AArch64)
- MacOS 12.3 and later (x86\_64, AArch64 via Apple Rosetta)

## 3. Differences from previous release

This release of Arm® Debugger contains the following changes.

- [New features](#)
- [Bug fixes](#)
- [Known issues](#)

### 3.1 New features

Arm Debugger 6.8.1 contains the following new features.

#### Processor and architecture support

No new processor and architecture support in this release.

#### Debugger functionality

No new features in this release.

For Arm® Development Studio 2025.1-1 users, there are additional new features described in the [Arm Debugger 6.7.0 Release Note](#) and the [Arm Debugger 6.8.0 Release Note](#).

### 3.2 Bug fixes

Arm Debugger 6.8.1 contains the following bug fixes:

- DSCORE-25287: Allow disabled cores in SMP connections

### 3.3 Deprecated and removed features

The following functionality is either deprecated or removed:

- Support for the Component Architecture Debug Interface (CADI) model connection interface has been removed. The only way to connect to Arm Fast Models and FVPs is now via the Iris model interface.
- The Target Configuration Editor is deprecated in this release, and will be removed in a future release.
- The `dbg_hw_batchupdater` command line utility is deprecated in this release, and will be removed in a future release. It will be replaced by a new DSTREAM configuration tool.
- Support of CMM commands used in a CMM script (\*.cmm) file is deprecated at this release, and will be removed in a future release.

## 3.4 Known issues

Arm Debugger 6.8.1 has the following known issues:

- Split DWARF is not supported for DWARF 5.
- It is your responsibility to install Windows drivers for the Olimex ARM-USB FTDI/JTAG devices. Following the advice from the manufacturer, you can download the [drivers from Olimex](#).
- When connected to Cortex®-R82 hardware via a debug probe, the `info memory` command shows an `NP` address space, but addresses in that space are not accessible.

### Windows

The first launch of Arm Debugger might trigger firewall warnings because it opens host platform sockets to a child process.

### Linux

- You must manually add udev rules for debug probe connections via USB. A set of relevant rules and an installation script (`usb-install`) are available in `<installation_directory>/sw/debughw/drivers`.
- There are known compatibility issues with SWT applications on Wayland display servers. For the Arm Development Studio IDE, if using a Wayland display server, it is recommended to force the use of X11 by setting the environment variable `GDK_BACKEND=x11` before launching. Alternatively, you can disable Wayland for your login session if possible.

### MacOS

The first launch of Arm Debugger might prompt you to allow network access because it opens host platform sockets to a child process.

## 4. Support

If you have any issues with the installation, content, or use of this release, create a ticket on <https://support.developer.arm.com>. Arm will respond as soon as possible.

You can raise queries and support issues relating to Arm Debugger on the [Arm Community website](#).



We provide support for this release of the product only to customers who have a current support and maintenance contract for the product.

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A Full release of the Arm Deliverable shall have met the contractual requirement for verification and validation of the deliverable subject to any waivers agreed between Arm and the Customer.



## 5. Release history

Provides a brief overview of the changes in previous versions of Arm® Debugger.

### 6.8.0

- Support for C1-Nano, C1-Pro, C1-Premium, C1-Ultra processors.

For more information, see the Arm Debugger [6.8.0 Release Note](#).

### 6.7.0

- New `jtag-scan` command.
- The `info registers` command now accepts symbols that resolve to registers, in addition to explicit register names.
- A new suite of Python scripts provide support for Coherent Mesh Network (CMN).
- Enhancements made to the ELA-600 and ELA-500 use case scripts for usability. For example:
  - Enables the use of a signal mapping file.
  - Enables the use of a pre-run script.
  - Outputs the decompressed and decoded data to a file. This file can be used to view the data in an external waveform viewer.

For more information, see the Arm Debugger [6.7.0 Release Note](#).

### 6.6.1

- Arm Debugger 6.6.1 contains support for the Altera Agilex 3 platform.

For more information, see the Arm Debugger [6.6.1 Release Note](#).

### 6.6.0

- Support for TPIU selection for off-chip trace. In Arm® Development Studio, DTSL trace options now present a selection for platforms with multiple TPIUs.

For more information, see the Arm Debugger [6.6.0 Release Note](#).

### 6.5.0

- Support for Cortex®-A320, Cache awareness for Neoverse™ V3 and Neoverse V3AE, and Masked watchpoint support for Armv8-A, Armv8-R, and Armv9-A.
- New `peripheral` command to manage peripheral register groups containing memory-mapped registers from a register frame.
- DSTREAM family DAP logging can now be enabled using the `--dap-log-path` option.
- New `delete` parameter to allow the `newvar` command to delete variables, register aliases, and register groups.

For more information, see the Arm Debugger [6.5.0 Release Note](#).

## 6.4.0armds

- Version of Arm Debugger available in Arm Development Studio 2024.1.

For more information, see the [Arm Debugger 6.4.0armds Release Note](#).

## 6.4.0

- Support for cache visualization for Cortex-X4
- New `snapshot` command to create a snapshot of CPU state, trace data, and system memory.
- Access the DSTREAM-ST USER IO pins using the RDDI JTAG\_SetConfigItems and JTAG\_GetConfigItems API.
- Retrieve the debug agent JTAG clock speed using the RDDI JTAG\_GetConfigItems API.

For more information, see the [Arm Debugger 6.4.0 Release Note](#).

## 6.3.0

- Support for Cortex-X925 and Cortex-A725.
- Support for AArch64 Linux kernel OS awareness up to 6.1.
- New controls to configure the semihosting security policy.
- `armdbg` is now capable of heterogenous multicore debug.
- Changes to the cache debug view and `cache print` command to hide invalid cache data lines.

For more information, see the [Arm Debugger 6.3.0 Release Note](#).

## 6.2.1

- This release of Arm Debugger contains only defect fixes that affect Arm Debugger 6.2.0.

For more information, see the [Arm Debugger 6.2.1 Release Note](#).

## 6.2.0

- Support for cache visualization for the Cortex-A720.
- Multiple Debug Server support.
- Support for accessing system registers using instruction encoding.

For more information, see the [Arm Debugger 6.2.0 Release Note](#).

## 6.1.3

- This release of Arm Debugger contains only defect fixes that affect Arm Debugger 6.1.2.

For more information, see the [Arm Debugger 6.1.3 Release Note](#).

### 6.1.2

- Support for the Cortex-R82AE, Cortex-A520AE, Cortex-A720AE, and Neoverse V3AE processors.
- Support for Armv9.5-A, including RME Granule Protection Check 2 Extension (FEAT\_RME\_GPC2) and Hardware Dirty State structure visualization (FEAT\_HDBSS and FEAT\_HACDBS).
- Support for debug connections to Arm Fixed Virtual Platforms v11.25.
- Support for Arm RD-1 AE platform configuration.

For more information, see the [Arm Debugger 6.1.2 Release Note](#).

### 6.1.1

- This release of Arm Debugger contains only defect fixes that affect Arm Debugger 6.1.0.

For more information, see the [Arm Debugger 6.1.1 Release Note](#).

### 6.1.0

- Support for Cortex-M52, Neoverse V3, and Neoverse N3 processors.
- Support 128-bit page tables (FEAT\_D128).
- Support for Armv9.5-A, including Float8 (FEAT\_FP8) and Fine-Grained Write Trap EL3 (FEAT\_FGWTE3).

For more information, see the [Arm Debugger 6.1.0 Release Note](#).

## Previous versions of Arm Debugger

Arm Debugger was previously released as a component of Arm Development Studio.

For details of previous updates to Arm Debugger, see the [Arm Development Studio Release Note](#).

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# Product and document information

Read the information in these sections to understand the release status of the product and documentation, and the conventions used in Arm documents.

## Product status

All products and services provided by Arm require deliverables to be prepared and made available at different levels of completeness. The information in this document indicates the appropriate level of completeness for the associated deliverables.

### Product completeness status

The information in this document is Final, that is for a developed product.

## Revision history

These sections can help you understand how the document has changed over time.

### Document release information

The Document history table gives the issue number and the released date for each released issue of this document.

#### Document history

Issue	Date	Confidentiality	Change
0681-00	27 February 2026	Non-Confidential	Release note update for Arm® Debugger 6.8.1
0680-00	5 December 2025	Non-Confidential	Release note update for Arm® Debugger 6.8.0
0670-00	20 November 2025	Non-Confidential	Release note update for Arm® Debugger 6.7.0
0661-00	13 August 2025	Non-Confidential	Release note update for Arm® Debugger 6.6.1
0660-00	29 May 2025	Non-Confidential	Release note update for Arm® Debugger 6.6.0
0641arnds-00	28 March 2025	Non-Confidential	Release note update for Arm® Debugger 6.4.1-arnds

Issue	Date	Confidentiality	Change
0650-00	28 March 2025	Non-Confidential	Release note update for Arm® Debugger 6.5.0
0640armds-00	17 December 2024	Non-Confidential	Release note update for Arm® Debugger 6.4.0-armds
0640-00	17 December 2024	Non-Confidential	Release note update for Arm® Debugger 6.4.0
0630-00	23 October 2024	Non-Confidential	Release note update for Arm® Debugger 6.3.0
0621-00	2 August 2024	Non-Confidential	Release note update for Arm® Debugger 6.2.1
0613-00	2 August 2024	Non-Confidential	Release note update for Arm® Debugger 6.1.3
0620-00	28 June 2024	Non-Confidential	Release note update for Arm® Debugger 6.2.0
0612-00	7 May 2024	Non-Confidential	Release note update for Arm® Debugger 6.1.2
0611-00	26 March 2024	Non-Confidential	Release note update for Arm® Debugger 6.1.1
0610-00	29 February 2024	Non-Confidential	First release as new document for Arm® Debugger

## Change history

For information about the functional changes to Arm Debugger, see the [Release History](#) section.

## Conventions

The following subsections describe conventions used in Arm documents.

### Glossary

The Arm Glossary is a list of terms used in Arm documentation, together with definitions for those terms. The Arm Glossary does not contain terms that are industry standard unless the Arm meaning differs from the generally accepted meaning.

See the Arm Glossary for more information: [developer.arm.com/glossary](https://developer.arm.com/glossary).

### Typographic conventions

Arm documentation uses typographical conventions to convey specific meaning.

Convention	Use
italic	Citations.
<b>bold</b>	Interface elements, such as menu names.  Terms in descriptive lists, where appropriate.
monospace	Text that you can enter at the keyboard, such as commands, file and program names, and source code.
monospace <u>underline</u>	A permitted abbreviation for a command or option. You can enter the underlined text instead of the full command or option name.
<and>	Encloses replaceable terms for assembler syntax where they appear in code or code fragments.  For example: <div>MRC p15, 0, &lt;Rd&gt;, &lt;CRn&gt;, &lt;CRm&gt;, &lt;Opcode_2&gt;</div>
SMALL CAPITALS	Terms that have specific technical meanings as defined in the <i>Arm® Glossary</i> . For example, <b>IMPLEMENTATION DEFINED</b> , <b>IMPLEMENTATION SPECIFIC</b> , <b>UNKNOWN</b> , and <b>UNPREDICTABLE</b> .



We recommend the following. If you do not follow these recommendations your system might not work.



Your system requires the following. If you do not follow these requirements your system will not work.



You are at risk of causing permanent damage to your system or your equipment, or harming yourself.



This information is important and needs your attention.



A useful tip that might make it easier, better or faster to perform a task.





**Remember**

A reminder of something important that relates to the information you are reading.

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# Useful resources

This document contains information that is specific to this product. See the following resources for other useful information.

Arm documents are available on [developer.arm.com/documentation](https://developer.arm.com/documentation).

Confidential documents are only available to licensees, when logged in. Each document link in the following tables provides direct access to the online version of the document.

Arm product resources	Document ID	Confidentiality
<a href="#">Arm Debugger Command Reference</a>	101471	Non-Confidential